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UPA-01228213-02
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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s) : **Pao-Lung Chen, Chen-Yi Lee**
Date : November 30, 2001
Serial No. :
Group Art Unit:
Filed :
For : **ARCHITECTURE OF METHOD FOR FETCHING
MICROPROCESSOR'S INSTRUCTIONS**



Honorable Commissioner of Patents
and Trademarks
Washington, D.C. 20231

INFORMATION DISCLOSURE STATEMENT

Sir:

Attached is a completed Form PTO-1449 and copies of the pertinent parts of the references cited thereon. Comments on any non-English-language references pursuant to Rule are submitted herewith.

Respectfully submitted

Applicants(s): pao-lung chen cyee _____

Encl:PTO-1449 & References

FORM PTO-1449 (Substitute)

ATTY. DOCKET NO.

UPA-01228

SERIAL NO.

LIST OF PRIOR ART CITED BY APPLICANT

(Use several sheets if necessary)

APPLICANT

Pao-Lung Chen, Chen-Yi Lee

FILING DATE

GROUP

JCE26 U.S. PTO
 10/024044
 12/16/01

U.S. PATENT DOCUMENTS

*EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE
	AA	5,687,339	Nov. 1997	Hwang	G06F	9/38	Sep. 1995
	AB						
	AC						
	AD						
	AE						
	AF						
	AG						
	AH						
	AI						
	AJ						
	AK						

FOREIGN PATENT DOCUMENTS

	AL						
	AM						
	AN						
	AO						
	AP						

OTHER PRIOR ART (Including Author, Title, Date, Pertinent Pages, Etc.)

	AR						
	AS						
	AT						

EXAMINER

DATE CONSIDERED

* EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.